

Electrical Properties of Granular Silicon

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Abstract: In the work, the electrical properties of granular silicon in the temperature range of ~ $20 \div 450$ ° C were experimentally investigated. It is show that the temperature dependence of conductivity is associated with the location of the granule. The proposed considerations and the results obtained are of significant interest in the study of micro and nanocrystalline semiconductors.

Keywords: *structura, specific resistance, semiconductor, potential barrier, move levels, localized traps, granula, conductivity of the traps, charge transfer.*

1. INTRODUCTION

Within the framework of representations developed in the first part of our work [1], we suggested simple answer to the issue what the mechanism of charge transfer in granular semiconductors is. It was believe that the processes of charge transfer in granular semiconductors depend on the size, structure and location of the granules. That is, the location of the granules can be in two forms, e.g., in series or in parallel next to each other. Accordingly, the transfer process of charge carriers in them is fundamentally different. In the first variant, i.e., if the granules are arrange in series, the transfer of the charge from the first granule to the second granule will occur through the two contacting regions. In this case, the increase in trapped charge on localized traps in the two contacting regions leads to an increase in the height of the potential barrier, and this simultaneously leads to an increase in the resistivity. To the second variant, if, the granules are arrange parallel to each other, the contacting area, also forms parallel between the granules as in the first variant, local energy levels are formed in it. However, in this case, the charge carriers do not move from one granule to the other, they are trapped in traps (E_i levels), that are parallel to the formed contacting regions, and move to E_i levels. Wherein, there is an increase in the total conductivity of the traps (Y_{ss}) . In this case, the overall specific conductivity is determined by the total specific conductivities. In this connection, in the present work, we discuss the experimental results of the charge transfer process in granular silicon, and the proposed mechanism is specifies.

2. RESEARCH METHOD

As mentioned in the previous article [1], there are a number of methods for obtaining granular semiconductor materials, of which the powder technology method is a promising method for producing polycrystalline semiconductor materials for solar cells or integrated circuits, as well as for thermoelements. Silicon granules were obtained in the framework of powder technology, which are given in work $[2 \div 5]$, and have been used by us for obtaining a polycrystalline silicon plate. As a raw material of the technology, single-crystal silicon substrates were use, where solar cells with a coefficient of about 15% are use on their basis. The starting samples are ground in a ball mill, wherein the powder to research structure of the granular silicon electronic microscope was used and to assess the transfer of charge carriers the specific resistance (ρ) of the granules was measured by two-probe method in a temperature range of 20 to 300 °C [4]. The measurements are conduct in semi-automatic mode directly with the granules heated, as in a temperature rise from 20 to 300 ° C, and at the stage of its decrease.

3. RESULTS AND DISCUSSION

As mentioned above and in the previous article [1], the granules can arranged in two variants, in series or parallel to each other. First, let us see the first variant.

Variant 1. Sequential location of granules. (Fig. 2). Usually such structures are observe larger sizes, for example, in granules with sizes of $100 \div 1000$ micrometers. Figure 1 illustrates the temperature dependence of the specific resistance (ρ) of granular silicon, (1 - 1000 μ m, 2 - 300 μ m and 3 - 100 μ m). From Fig. 1, it can see that the specific resistance in all cases increases with temperature. With decreasing granules size ($1 \rightarrow 2 \rightarrow 3$), the growth in specific resistance is observed more strongly than in others.



Fig1. Dependence of the specific resistance on temperature.

From the proposed mechanism for the emergence of carrier-charge transfer in granular semiconductors, in the first part of our work [1] it is known that the specific resistance is described by the expression:

$$\rho = \frac{k}{q\langle a \rangle A^*T} \exp(\frac{q\varphi}{kT}) \tag{1}$$

where $\langle a \rangle$ is the grain size, φ is the potential barrier height on the grain boundary.

The potential barrier height

$$\varphi = \frac{Q_i^2}{8\varepsilon_o q N_G} \tag{2}$$

Here, Q_i is the boundary derivative charge in the grain boundaries, N_G - is the concentration of an electrically active dopants, ε_0 and ε – dielectric constant.



Fig2. Simplified scheme of location of the granule (a), zone diagram (b). (1, 2, 3) - silicon granules, 4 - ceramic tube, M_A and M_V ohmic contacts respectively, in the A and B regions.

It is seen from (1) that an increase in the trapped charge Q_i on localized traps in the boundaries between the granules, that is, in the two contacting regions of the granule, leads to an increase in φ , and this simultaneously leads to an increase in ρ (Fig. 2a). An equivalent electrical scheme of such a structure is presented on fig.2. It can be seen that the equivalent electrical scheme consists of the series-connected resistances of the granule and the two contacting regions of the granule. In this case, the total specific resistance is determined by the total specific resistances. In addition, it can be seen from (1) that the specific resistance is related to the granule size $\langle a \rangle$, that is, a decrease in the granule size $\langle a \rangle$ leads to an increase in ρ . From Fig. 1, it is seen that with a decrease in the size of the granule $(1\rightarrow 2\rightarrow 3)$, the growth of resistivity is observed more strongly than in others. This indicates that an increase in the trapped charge Q_i on localized traps in the boundaries between the granules, in smaller sizes is observed more strongly.

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Variant 2. Arrangements of granule next to each other (in Fig. 6). Usually such structures are observed in smaller sizes, for example, in granules with dimensions of $400 \div 700$ micrometers. Figure 3 illustrates the temperature dependence of the specific conductivity (σ) of granular silicon, it is very different from the first. From Fig. 3, it can be seen that the specific conductivity and the dark current increases with temperature. (Fig. 4). It also shows a decrease in voltage and growth (Figure 5). To explain the results, we use the model of thermionic emission [4÷7] with additions concerning the account of the currents arising in the process of capture and emission of charge carriers in traps, which we formulated in [1], and also by a simplified sample scheme (Figure 6a) and the zone diagram between the charged granules (Fig. 6b).



Fig3. Dependence of the conductivity on temperature.

It is known that for semiconductors the decrease in the resistivity is associated with the onset of intrinsic conductivity with increasing temperature. In addition, this increases the concentration of charge carriers and this in turn leads to an increase in current. The result of the research confirms this mechanism. However, according to the location of the granules, as described in the first part of our work [1] (Fig. 5), the charge transfer process must occur in two connected regions.



Fig4.Dependence of dark current and of voltage on temperature.

From Fig. 5b, it follows that charge carriers are capture by states (E_i) at the boundary region. Emerged charge is compensate by ionized charges located in the space charge region. During the capture and emission of charge carriers on traps observed J_{ss} currents arise and they move along E_i levels located in region 5 (Figure 5). The current J_{ss} is:

$J_{SS} = Y_{SS} \delta \varphi$

where Y_{ss} is the characteristic total conductivity of the traps, which depends both on their capture cross section and on the energy distribution, as well as on the position in space, that is, the location in region 5 (Fig. 5), $\delta \varphi$ is the change in the height of the potential barrier.

The current J_{ss} is identically equal to the derivative in time with respect to charge bound on the boundary surface. In region, 5 (Fig. 5) the following situation takes place [4, 6, 8]: to the process of whole capture and emission on the boundary surface, the width of the spatial charge region should vary in order for total electroneutrality to be kept. This, in turn, influences the whole zone diagram (Fig. 5b), i.e., as a change in the barrier height $\delta\varphi$, so and on Y_{ss} . This means that the current J_{ss} and the change in the barrier height $\delta\varphi$ are interrelated, and the vibrational properties of this interrelation are determined by the properties of the traps. Naturally, in the process of temperature change, both capture and emission of charge carriers with the participation of traps are observe. When the number of charge carrier retention is greater than their emission, the motion of the charge along the levels of the traps along the boundary of the traps. If the emission prevails over the capture, then the conductivity of the traps also increases, despite the fact that the charge moves in the opposite direction.



Fig5. Simplified scheme of location of the granule (a), zone diagram (b). (1, 2, 3) - silicon granules, 4 - ceramic tube, M_A and M_B contacts, respectively, in the A and B regions.

In this work [4, 6, 8], we studied the manifestations of the energy states conditioned by traps, for example, with $E_{i1} \sim 0.15 \ eV$, $E_{i2} \sim 0.17 \ eV$, $E_{i3} \sim 0.36 \ eV$, $E_{i4} \sim 0.3 \ eV$ which are manifested at temperatures T~70÷420 °C. Current changes for example, growth at T≤150 °C, T~180÷230 °C, T~240÷280 °C, T~290÷300 °C, T~310÷380 °C, T≥400 °C, decrease at T~150÷170 °C, sharp growth at T~180 °C, T~240 °C, T~290 °C, T~310 °C, and also voltage changes at these temperatures correspond to manifestations due to the energy states E_{i1} , E_{i2} , E_{i3} , E_{i4} . The outgoing carriers from the valence band Ev are capture in E_{i1} and they successively move through levels that exhibit states E_{i2} , E_{i3} , E_{i4} with temperature changes (Fig. 5b). This process occurs before these levels are fill. During the transition of charges over levels, the current J_{ss} and the conductivity of the Y_{ss} traps [4÷7] arise, and this simultaneously leads to an increase in the conductivity.

4. CONCLUSION

Thus, the electrical properties depend on the granule size and location.

If the granules are arranges sequentially, the total specific resistance determined by the sum of all specific resistances. I.e, the specific resistance is relating to the granule size $\langle a \rangle$, that is, a decrease in the granule size $\langle a \rangle$ leads to an increase in ρ .

If the granules are parallel to each other, the charge carriers do not move from one granule to another, they are retention in traps and move along the E_i levels in region two connected granules. During the transition of charges over levels, the current J_{ss} and the conductivity of the Y_{ss} traps arise, and this simultaneously leads to an increase in the conductivity.

Thus, the comparison of the qualitative theory given in [1] with the experiments discussed here shows almost complete coincidence, the simplicity of the mechanism and its correspondence to other phenomena in granular semiconductors indicate its viability.

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